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From: Jacqueline J. Garner
Texas Instruments Incorporated
Phone: 214-532-9348

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**FEB 21 2005**

Applicant: Chambers
Serial No.: 10/696,539
Filing Date: 10/29/2003
Customer No.: 23494
Title: METHODS FOR FABRICATING A TRIPLE-GATE MOSFET TRANSISTOR

Art Unit: 2818
Examiner: Le, Dung Anh
Docket No.: TI-36031
Conf. No.: 7816

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Jacqueline J. Garner, Reg. No. 36,144February 21, 2005
Date**FACSIMILE COVER SHEET**

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT EPQ (7 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Pages)
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<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Chambers	
TITLE OF INVENTION: Methods for Fabricating a Triple . . .	
TI FILE NO.: TI-36031	DEPOSIT ACCT. NO.: 20-0668
FAXED: 2/21/2005 DUE: 2/22/2005 ATTY/SECY: JJG	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/696,539 Filing Date: 10/29/2003 Conf. No.: 7816	

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074

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Ex Parte Quayle Amendment

Assistant Commissioner of Patents
Washington, DC 20231
February 21, 2005

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
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the U.S. Postal Service as First Class Mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on
February 21, 2005


Jacqueline J. Garner, Reg. No. 36, 144

The following amendments and remarks are offered in response to the Examiner's Office Action dated 12/22/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

The **Amendments to the Specification** begin on page 2.

The **Amendments to the Abstract** begin on page 3.

Amendments to the Claims are reflected in the listing of claims that begin on page 4 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 7.